Attorney's Docket No.: 07977/192001/US3444



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani, et al.

Art Unit:

2811

Serial No.: 08/958,568

Examiner:

S. Hu

\$0

\$0

Filed

: October 28, 1997

Title

: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME

Commissioner for Patents Washington, D.C. 20231

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims 53 - 66 = 0
Independent 11 - 11 = 0

TOTAL FEE DUE \$0

Respectfully submitted,

Date: 7/6/00

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

July 6, 2000
Date of Deposit

Signature

Susan Regan

Typed or Printed Name of Person Signing Certificate

JUL 1 0 2000

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Assistant Commissioner for Patents Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

In response to the Official Action mailed June 6, 2000, applicants elect the invention of claims 60-62, and 72, the election is made without traverse.

In the Claims:

Please cancel claims 13, 14, 19-28, 56-59, and 63-72.

Please add the following new claims.

--73. (New)

semiconductor device comprising:

a substrate;

a source region and a drain region provided over said

substrate;

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